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PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: VON AMMON ET AL -9

EXAMINER: J. MONDT

SERIAL NO.:

09/813,240

GROUP: 2826

FILED:

MARCH 20, 2001

TITLE:

SEMICONDUCTOR WAFER MADE FROM SILICON AND METHOD FOR

PRODUCING THE SEMICONDUCTOR WAFER

## SUBMISSION OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT OF DR. WOLFGANG STAUDACHER

Mail Stop Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants wish to bring to the attention of the Patent Examiner the attached Information Disclosure Statement, duly signed by Dr. Wolfgang Staudacher, along with the eight references and their corresponding English Language Abstracts, listed on the enclosed Form PTO-1449 and attached thereto.

The relevance of these references is that they were cited in the corresponding Japanese patent application and the Office Action of the Japanese Patent and Trademark Office

Since this Information Disclosure Statement is being filed after a first Office Action, the official fee of \$180.00 is enclosed. The Commissioner is hereby authorized to charge Deposit Account No. 03-2468 for any additional fees or credit any overpayment in connection with this IDS.



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It is respectfully requested that the foregoing Information Disclosure Statement be incorporated into the official file of the present patent application.

Respectfully submitted, Wilfred VON AMMON ET AL

COLLARD & ROE, P.C. 1077 Northern Boulevard Roslyn, New York 11576 (516) 365-9802 Allison C. Collard, Reg. No. 22,532 Edward R. Freedman, Reg. No. 26,048 Elizabeth C. Richter, Reg. No. 35,103 Attorneys for Applicant

Enc: 1) Check in the amount of \$180.00

- 2) Supplemental Information Disclosure Statement of Dr. Wofgang Staudacher
- 3) Form PTO-1449
- 4) 8 references with English Language Abstract

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, P.O. BOX 450, Alexandria, VA 22313 -1450, on February 9, 2004.

Maria Guastella

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In re application of: VON AMMON ET AL

Serial Number: 09/813,240

Filed: March 20, 2001

For: SEMICONDUCTOR WAFER MADE FROM SILICON AND METHOD FOR

PRODUCING THE SEMICONDUCTOR WAFER

## Information Disclosure Statement

Group Art Unit: 2811

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir or Madam:

I, Wolfgang Staudacher, associated with the preparation and prosecution of the above-identified application, residing at Seebauerstraße 4, 81735 München, Germany, wish to call the attention of the Patent Examiner to the references enumerated on the enclosed PTO Form-1449.

I believe the documents enumerated on the enclosed Form PTO-1449 and attached thereto, are cited in the enclosed Office Action of the Japanese Patent and Trademark Office and may be material to the examination of the application.

Therefore, it is respectfully requested that the foregoing Information Disclosure Statement be considered by the Examiner and incorporated into the file of this application.

I wish to comment as follows concerning the prior art references enumerated on PTO Form-1449:

For JP 11-189495 a Patent Abstract of Japan is enclosed.

For JP 07-247197 a Patent Abstract of Japan is enclosed.

For JP 07-066256 a Patent Abstract of Japan is enclosed.

For JP 07-126094 a Patent Abstract of Japan is enclosed.

For JP 57-017497 a Patent Abstract of Japan is enclosed.

For JP 05-294780 a Patent Abstract of Japan is enclosed.

For JP 06-271399 a Patent Abstract of Japan is enclosed.

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For JP 10-098047 a Patent Abstract of Japan is enclosed.

Signed this 14 tay of November 2003.

Dr. Wolfgang Staudacher

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FORM PTO-1449 (REV. 7-80)

ATTY. DOCKET NO.: ST 10015

SERIAL NO. 09/8/3,240

VON AMMON ET AL-9

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 509; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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FORM PTO-1449 (REV. 7-80)

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